

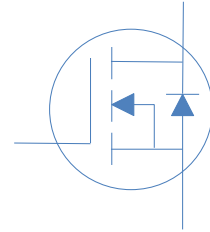
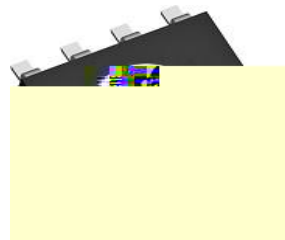
120V N-Ch Power MOSFET

oF F F o F
o F F F
o G o F
F F F F F
F F

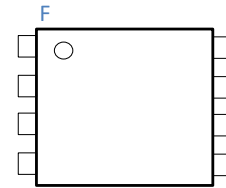
V_{DS}	120	V
$R_{DS(on),typ}$	34.0	m
I_D (Silicon Limited)	6	A

G

o F ffl F F
F o F F oF F
F F F F



Part Number	Package	Marking
HGS380N12S	SOIC-8	GS380N12S



Absolute Maximum Ratings at T_J

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	I_D	T_C	6	A
		T_C	4	
Drain to Source Voltage	V_{DS}	-	120	V
Gate to Source Voltage	V_{GS}	-	20	V
Pulsed Drain Current	I_{DM}	-	60	A
Avalanche Energy, Single Pulse	E_{AS}	$L=0.1mH, T_C$	5	mJ
Power Dissipation	P_D	T_C	3.1	W
Operating and Storage Temperature	T_J, T_{stg}	-	-55 to 150	

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Lead	R_{JL}	23	
Thermal Resistance Junction-Ambient (steady state)	R_{JA}	40	
		75	

Electrical Characteristics at T_j
Static Characteristics

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\text{ A}$	120	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\text{ A}$	2	3.0	4.0	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS}=0V, V_{DS}=120V, T_j$	-	-	1	A
		$V_{GS}=0V, V_{DS}=120V, T_j$	-	-	100	
Gate to Source Leakage Current	I_{GSS}	$V_{GS} \quad \quad \quad V_{DS}=0V$	-	-	100	nA
Drain to Source on Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=10A$	-	34	38	m
Transconductance	g_{fs}	$V_{DS}=5V, I_D=10A$	-	20	-	S
Gate Resistance	R_G	$V_{GS}=0V, V_{DS}\text{ Open}, f=1\text{MHz}$	-	3.60	-	

Dynamic Characteristics

Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=60V, f=1\text{MHz}$	-	578	-	pF
Output Capacitance	C_{oss}		-	77	-	
Reverse Transfer Capacitance	C_{riss}		-	6.4	-	
Total Gate Charge	$Q_g(10V)$	$V_{DD}=60V, I_D=5A, V_{GS}=10V$	-	6.8	-	nC
Gate to Source Charge	Q_{gs}		-	2.3	-	
Gate to Drain (Miller) Charge	Q_{gd}		-	0.7	-	
Turn on Delay Time	$t_{d(on)}$	$V_{DD}=60V, I_D=5A, V_{GS}=10V,$ $R_G=10\text{ }\Omega$	-	7	-	ns
Rise time	t_r		-	8	-	
Turn off Delay Time	$t_{d(off)}$		-	11	-	
Fall Time	t_f		-	8	-	

Reverse Diode Characteristics

Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_F=20A$	-	0.9	1.2	V
Reverse Recovery Time	t_{rr}	$V_R=60V, I_F=5A, di_F/dt=500A/\text{s}$	-	27	-	ns
Reverse Recovery Charge	Q_{rr}		-	97	-	nC

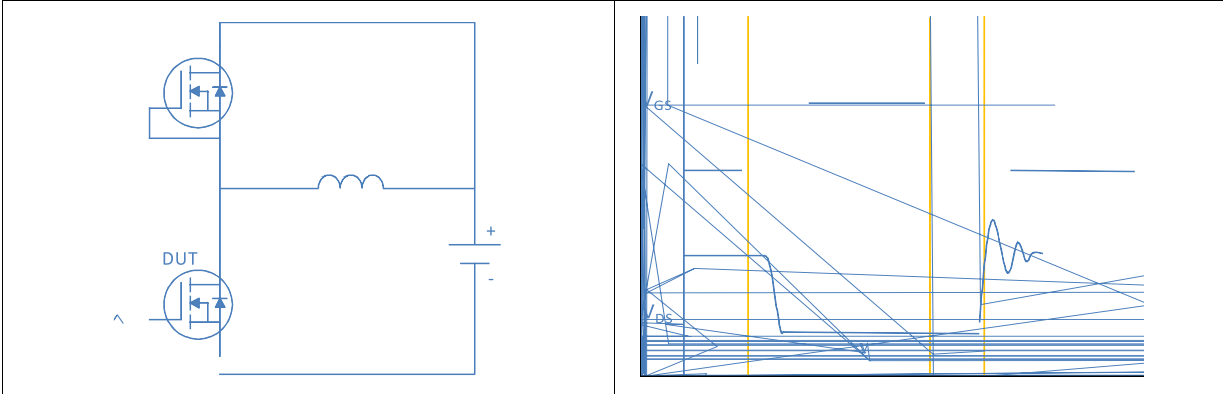


Figure 9. Maximum Safe Operating Area

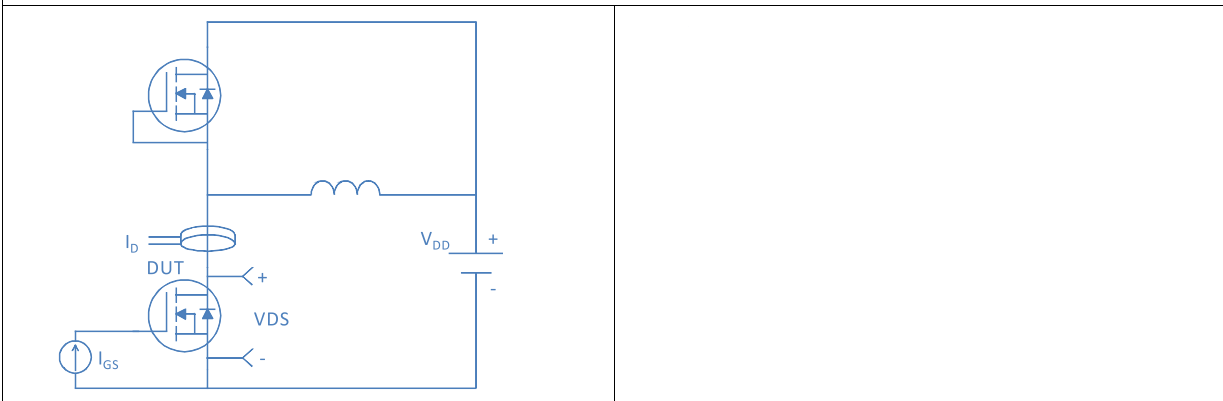
Figure 10. Maximum Drain Current vs. Case Temperature

Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient

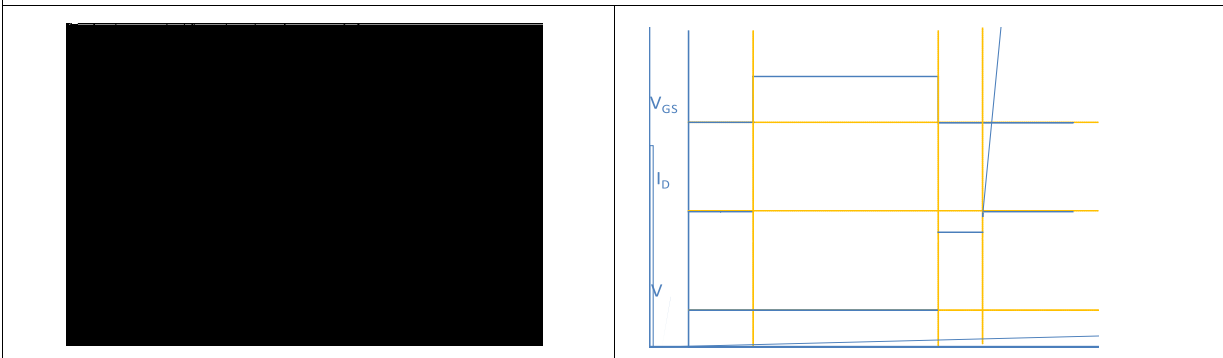
Inductive switching Test



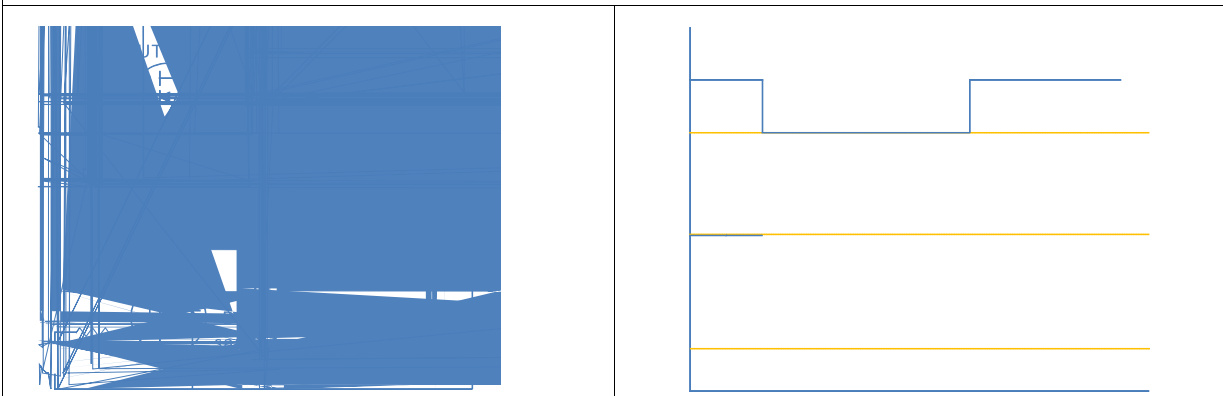
Gate Charge Test



Uclamped Inductive Switching (UIS) Test

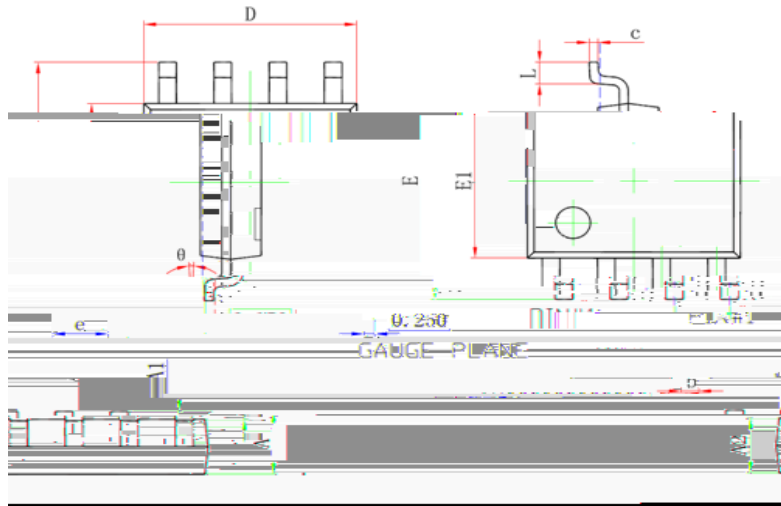


Diode Recovery Test



Package Outline

SOIC-8, 8 leads



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.250	1.650	0.049	0.065
b	0.310	0.510	0.012	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (SBC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.031
theta	0°	8°	0°	8°